BOARD OF TECHNICAL EDUCATION, DELHI

No. F. 53(G)/Exam/BTE/2025

Dated : 18/06/25

Electronic Engineering(Digital Electronics & Microprocessor System Design)

Performance of the candidates (Compartment) of I semester of three years Diploma Course in Electronic Engineering(Digital Electronics & Microprocessor Syswho have appeared for the examination held in JAN 2025 is communicated as under. The marks of candidates who have cleared full subjects out of an aggregate of 1300 are indicated against their roll numbers.

If there is any discrepency, that should be brought to the knowledge of Board within FIFTEEN DAYS—from the date of publication of result.
NO CASES WILL BE ENTERTAINED AT LATER STAGE.

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| ; Roll No ; Subjects in which declared passed / Mark | s Obtained |
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NOTE :

- 1. Eligible candidates who have not been successful in any of the subject or have not appeared in any subject are notified as failed
- 2. The performance of the candidates who have been placed under P.L.O. will be communicated separately, if necessary.

SUBJECT CODES :

Theories

ED170 Mathematics - I

ED171 Applied Physics - I

ED172 Applied Chemistry

ED173 Communication Skills in English

Practicals

ED180 Applied Physics Lab - I

ED181 Applied Chemistry Lab - I

ED182 Communication Skills in English Lab

ED183 Engineering Graphics

ED184 Engineering Workshop Technology

ED185 Sports and Yoga

Forwarded to the Principal, Chhotu Ram Rural Institute of Technology, Kanjhawla, Defor information and necessary action.

(S. P. MEHER) CONTROLLER

